

ABSTRACT OF THE DISCLOSURE

A semiconductor integrated circuit device comprises a first column gate circuit that electrically connects a first bit line group to a data line group according to a first column selection signal, a second column gate circuit that electrically connects a second bit line group to the data line group according to a second column selection signal, word lines that intersect the bit lines and memory cells that are electrically connected to the bit lines are selected by the word lines and include magneto-resistive elements. The spinning directions of the magneto-resistive elements are perpendicular to the bit lines as seen in a plan view.